Two-Terminal Millimeter-Wave Sources
Heribert Eisele and George I. Haddad, Life Fellow, IEEE

Abstract—Basic principles of operation, fundamental power-generation capabilities, and fabrication technologies are reviewed for three groups of two-terminal devices, i.e., resonant-tunneling diodes (RTD’s), transferred-electron devices (TED’s), and transit-time diodes. The paper focuses on devices for frequencies above 30 GHz, and an overview of recent research in this area and of various state-of-the-art laboratory results is given. As an outlook, the potential of some new material systems for high-power devices is discussed.

Index Terms—Etching, gallium compounds, Gunn devices, IMPATT diodes, indium compounds, millimeter-wave devices, millimeter-wave generation, millimeter-wave oscillators, oscillator noise, phase noise, silicon, silicon compounds, submillimeter-wave devices, submillimeter-wave generation, submillimeter-wave oscillators.

I. INTRODUCTION

Despite the rapid progress in the upper frequency limits and RF power levels of three-terminal devices [1]–[5], two representatives of two-terminal devices, i.e., Gunn devices and impact avalanche transit time (IMPATT) diodes, together with vacuum tubes, still play an important role in many system applications. Three-terminal devices have almost entirely superseded two-terminal devices in low-noise preamplifiers, up to millimeter-wave frequencies. This paper will mainly focus on oscillators and will give an overview of resonant-tunneling diodes (RTD’s), Gunn or transferred-electron devices (TED’s), and transit-time diodes, as well as on their fundamental power-generation capabilities at millimeter-wave frequencies. Various power-combining techniques were utilized to increase the available RF output power, and examples will be presented for each of the discussed two-terminal devices.

II. GENERAL TECHNOLOGY

Selective etching technologies with the benefits of, e.g., well defined and highly reproducible mechanical dimensions of the device mesa and contact structures, are employed in the fabrication of most of the present state-of-the-art two-terminal devices. The flowchart of Fig. 1 serves as an example and illustrates the fabrication steps for GaAs IMPATT or tunnel-injection transit-time (TUNNETT) diodes on integral heat sinks [6], [7]. More detailed descriptions of various fabrication technologies for devices in the Si-, GaAs-, and InP-material systems or references to them are given in [6]–[11]. In the first step, as shown in Fig. 1, the metallization for the p-ohmic contact (Ti/Pt/Au) is evaporated or sputtered onto the surface. A thick gold layer is then electroplated onto this metallization to form the integral heat sink. The sample is mounted on a carrier to provide additional mechanical support and to protect the heat sink from etchants during the subsequent fabrication steps. The substrate is removed in a well-known selective etchant of \( \text{H}_2\text{O}_2 : \text{NH}_4\text{OH} 1 : 19 \) [12], [13], which does not significantly attack the Ga\textsubscript{x}Al\textsubscript{1-x}As stop-etch layer if \( x > 0.4 \).

Ohmic contacts are more difficult to form on Ga\textsubscript{x}Al\textsubscript{1-x}As than on GaAs. Therefore, this Ga\textsubscript{x}Al\textsubscript{1-x}As layer is selectively removed in a solution of HF, which does not attack GaAs. A photolithography step defines the openings on this heavily n+-doped GaAs layer, where the standard metallization (Ni/Ge/Au/Ti/Au) for the n-ohmic contacts is deposited. Excess metal outside the contacts is lifted off with the photoresist and, using another photolithography step, each contact is selectively electroplated with several microns of gold to form a good bonding pad. The contact pad then acts as a mask when the mesa of the diode is etched in a nonsel ective etchant. After the sample has been removed from the carrier, the contacts are annealed, and the sample is diced into individual diodes. Diodes are then mounted in packages for appropriate RF circuits.

Proper thermal management is one of the most critical issues in high-power two-terminal devices, and high-performance devices for reliable long-term operation are generally mounted on diamond heat sinks [8]–[11], [13]–[18]. Typically, heat-flow resistances of mesa-type devices on diamond heat sinks are at least a factor of two lower than those of devices on integral heat sinks.

III. RTD’S

RTD’s utilize the distinctive properties of the carrier transport across a double heterojunction barrier [19], as illustrated in Fig. 2, for the InGaAs/InAlAs and InGaAs/AlAs material systems. If, under bias, one of the discrete energy levels in the quantum well between the barriers lines up with the conduction band outside, a large (resonant-) tunneling current of electrons will flow from the band outside through the barriers [19]. This process causes current–voltage characteristics, as shown in Fig. 2, for the RTD’s with high peak-to-valley ratios (PVR’s) \( J_{p}/J_{v} \) in the InAlAs/InGaAs and AlAs/InGaAs material systems. The RTD exhibits a negative differential resistance if biased between the \( V_{p} \), the voltage for the peak current density \( J_{p} \), and \( V_{v} \), the voltage for the valley current density \( J_{v} \). Since this (resonant-) tunneling process is very fast, this negative resistance exists from dc up to submillimeter-wave frequencies. This very broad-band negative resistance...
Fig. 1. Flowchart with the fabrication steps for GaAs IMPATT or TUNNETT diodes on integral heat sinks [6], [7]. (a) Island definition, p-ohmic evaporation and gold plating of heat sink to approximately 20 μm. (b) Substrate thinning, stop-etch layer removal, and n-ohmic evaporation. (c) Gold plating of ohmic contacts. (d) Final diodes after mesa etch and annealing.

Fig. 2. Conduction band profiles and current–voltage characteristics for lattice-matched InAlAs/InGaAs and strained AlAs/InGaAs RTD’s with high PVR’s. Easily leads to bias–circuit oscillations, and a simple analysis [6] reveals two conflicting power limitations in these diodes. A simplified equivalent circuit for an oscillator with a resonant circuit as the load and a series resistance \( R_o \) for the combined losses, contact resistances, and so forth is shown in Fig. 3, and includes relevant elements of the bias circuit. Operation in an oscillator at such a high-frequency \( \omega \) is assumed so that the capacitance (per unit area) \( C_D \) of the
depleted region becomes dominant, i.e.,

$$\omega C_D \frac{V_u - V_p}{J_p - J_v} \gg 1. \quad (1)$$

Generally, the smallest load resistance $R_L$ that can be presented to the two-terminal device determines the maximum device area $A$. In this case, the RF circuit limits the RF output power $P_{RF}$, and with the specific series resistance $\rho_s = \frac{R_e A}{L_S}$

$$P_{RF} = \frac{1}{8\omega^2C_D} \left( J_p - J_v \right)^2 \frac{R_L}{\left( R_L + \frac{\rho_s A}{L_S} \right)^2}. \quad (2)$$

However, to avoid oscillations in the bias circuit, the following much smaller area must be chosen [6]:

$$A = \frac{\rho_s C_D}{L_S} \frac{J_p - J_v}{V_u - V_p} \quad (3)$$

which severely reduces the power to

$$P_{RF} = \frac{\rho_s C_D}{8L_S} \left[ 1 + \rho_s \frac{\omega^2 C_D (V_u - V_p)}{J_v - J_p} \right] (V_u - V_p)^2. \quad (4)$$

RTD’s in waveguide circuits are, in general, contacted by a whisker [20], and this whisker inductance is a major contribution to $L_S$ (3), (4), which cannot be reduced much.

RTD’s for oscillators were realized in the InGaAs/AlAs, GaAs/AlAs, and InAs/AlSb material systems. Oscillation frequencies up to 712 GHz with InAs/AlSb RTD’s [20] are the highest achieved to date and exceed those of any other two-terminal (and also three-terminal) devices. As illustrated in Fig. 4, RF power levels are low, although power combining was shown to bring about considerable improvement [21] and, as an example, the RF power of 28 $\mu$W was measured at 290 GHz [22]. In this monolithic approach, part of the bias circuitry is integrated and helps reduce $L_S$. The highest RF power levels of 200 $\mu$W at 100 GHz and 50 $\mu$W at 205 GHz were reported from InGaAs/AlAs RTD’s [23]. Low output power levels, together with the inherent tendency for bias instabilities have, so far, severely limited the use of RTD’s in system applications, and only one experiment with an RTD in a receiver system was reported [24].

IV. TED’s

The operation of TED’s is based on a specific conduction-band structure with several minima (valleys) that allow an electron transfer from a central valley with low effective mass and high mobility to at least one satellite valley with high effective mass and low mobility. This type of transfer is possible in many compound semiconductor materials [25], but, so far, only GaAs and InP have played a major role. Acceleration–deceleration and energy relaxation times in the range of 0.4–1.5 ps for GaAs and 0.2–0.75 ps for InP [6], [26], [27] govern this intervalley transfer process, cause a so-called “dead zone” in the active region [6], and impose a fundamental frequency limit on Gunn devices. As illustrated in Fig. 5 for a W-band (75–110 GHz) InP Gunn device with a 1.7-$\mu$m-long active region, the dead zone can be regarded as an additional series resistance that diminishes the dynamic negative resistance of the device. Current-limiting contacts [14], [26], [27] and heterojunction barriers [28] are examples of experimentally tested approaches to a substantial reduction of this dead zone. The fundamental frequency limit is estimated to be near 100 GHz in GaAs TED’s and near 200 GHz in InP TED’s [6], [26], [27] and corroborated by experimental results with fundamental-mode operation exceeding 80 GHz in GaAs TED’s [16], [28] and exceeding 160 GHz in InP TED’s [7], [18]. Due to their excellent noise properties in medium-power oscillators, TED’s have found widespread use as local oscillators and drivers for multiplier or amplifier chains in many system applications. Fig. 6 summarizes the state-of-the-art RF power levels from InP and GaAs TED’s above 30 GHz. Fundamental-mode operation suffers a sharp decline in the RF performance above approximately 70 GHz, as seen in GaAs TED’s, and above approximately 140 GHz, as seen in InP TED’s, which presages the above fundamental frequency limits. However, operation in a second-harmonic-mode has been proven to be very effective in extending the useful frequency range of TED’s with RF power levels of, e.g., 96 mW at 94 GHz [29] and 7.5 mW at 180 GHz [30].
Various experimental results at millimeter-wave frequencies demonstrated that power combining with Gunn devices (in particular, the fundamental mode) is straightforward and that high combining efficiencies (even exceeding 100%) can be easily obtained up to the highest frequencies [31], [32].

V. TRANSIT-TIME DIODES

A confined bunch of carriers is generated in a narrow region (by different means, depending on the type of diode [6], [25]) and its transit through a depleted region induces a current flow in the outer circuit. The phase delay between the RF voltage at the terminals and the induced current results in a dynamic negative resistance and the generation of RF power. IMPATT diodes with significant power levels have been realized with the semiconductor materials Si, GaAs, and InP. Si and GaAs IMPATT diodes are commercially available. Fig. 7 summarizes the state-of-the-art RF power levels from various transit-time diodes under CW operation in the frequency range of 30–400 GHz. Numbers next to the symbols denote dc-to-RF conversion efficiencies in percent.

Fig. 6. Published state-of-the-art RF power levels from TED’s under continuous wave (CW) operation in the frequency range of 30–300 GHz. Numbers next to the symbols denote dc-to-RF conversion efficiencies in percent.

Fig. 7. Published state-of-the-art RF power levels from various transit-time diodes under CW operation in the frequency range of 30–400 GHz. Numbers next to the symbols denote dc-to-RF conversion efficiencies in percent.

Diodes reach oscillation frequencies above 300 GHz [33]. Many of the state-of-the-art RF power levels from Si, but also GaAs IMPATT diodes in Fig. 7, were the results of major research and development efforts in the late 1970’s and 1980’s [14], [15], [33]. Recent research in the area of Si and GaAs IMPATT diodes focuses on refining the state-of-the-art by improving fabrication technologies and by employing advanced growth techniques such as molecular-beam epitaxy (MBE) to implement more complex doping profiles [8], [11], [15], [17], as illustrated in Fig. 8. Such doping profiles increase the available impedance level for the same diode area and
improve dc-to-RF conversion efficiency as well as RF output power. Conversely, higher dc-to-RF conversion efficiencies help reduce the dc power consumption and ease the thermal constraints of planar oscillators [34] where most of the heat has to be dissipated into the semiconductor material with a typically much lower thermal conductivity than most metals.

Contrary to RTD’s and TED’s, IMPATT diodes have also found widespread use in applications where they are driven with pulses. This mode of operation overcomes thermal limits and results in significantly higher peak RF power levels such as 28 W at 35 GHz [14], 42 W at 96 GHz [11], and 5.6 W at 140 GHz [14] from Si IMPATT diodes under short 50–200-ns-long pulses.

Numerous power-combining techniques were extensively investigated and subsequently employed in Si and GaAs IMPATT amplifiers and oscillators. A review of power-combining techniques can be found in [31]. Examples of CW operation are the RF output power of 20 W at 44 GHz for an output stage of 16 Si IMPATT diodes [35] and 0.6 W at 60.5–62.5 GHz for an output stage of eight GaAs IMPATT diodes [36].

### VI. NEW MATERIALS

Considerably higher RF power levels, in pulsed mode in particular, can be expected from transit-time diodes if wide-bandgap materials such as SiC, GaN, and diamond are used. As is well known [25], the RF power generated from a transit-time diode is proportional to the figure of merit $M_p = \left(\frac{E_c v_a}{f}\right)^2$. Table I summarizes relevant material parameters of semiconductors that can play an important role for transit-time diodes. Their power-generation capabilities are compared in terms of $M_p$ relative to Si. However, significant additional efforts are required in the areas of material growth and characterization, as well as doping and processing technologies before the full potential of these wide-bandgap materials can be utilized.

#### VII. SELECTED EXPERIMENTAL RESULTS

Research at the University of Michigan at Ann Arbor contributed to, or resulted in, several state-of-the-art results from two-terminal devices. RF power levels of more than 200 mW at 103.1 GHz [32] and, as shown in Fig. 9, power levels of more than 130 mW around 132 GHz and more than 80 mW at 152 GHz (with low FM noise measures and very low phase noise) were achieved with InP Gunn devices on diamond heat sinks [18]. These RF power levels are the highest reported to date from any Gunn devices and they correspond to dc-to-RF conversion efficiencies exceeding 2.3% between 102–132 GHz. Four results from the first

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**Table I**

<table>
<thead>
<tr>
<th>Property</th>
<th>Si</th>
<th>GaAs</th>
<th>InP</th>
<th>6H-SiC (4H-SiC)</th>
<th>GaN</th>
<th>Diamond</th>
</tr>
</thead>
<tbody>
<tr>
<td>Band Gap (eV) (@ 300 K)</td>
<td>1.12</td>
<td>1.42</td>
<td>1.34</td>
<td>3.06 (3.26)</td>
<td>3.39</td>
<td>5.5</td>
</tr>
<tr>
<td>Electron mobility (@ 300 K, cm²/Vs)</td>
<td>1400</td>
<td>8500</td>
<td>4600</td>
<td>400 (850)</td>
<td>80 (1020)</td>
<td>900</td>
</tr>
<tr>
<td>Hole mobility (@ 300 K, cm²/Vs)</td>
<td>450</td>
<td>400</td>
<td>140</td>
<td>90 (115)</td>
<td>150</td>
<td>1600</td>
</tr>
<tr>
<td>Breakdown field $E_c$ (V/cm) (@ $N_D \sim 10^{17}$ cm⁻³)</td>
<td>0.61</td>
<td>0.65</td>
<td>0.75</td>
<td>2.5 (2.2)</td>
<td>2</td>
<td>10</td>
</tr>
<tr>
<td>Thermal conductivity (W/cmK)</td>
<td>1.25</td>
<td>0.46</td>
<td>0.68</td>
<td>4.9</td>
<td>1.3</td>
<td>20</td>
</tr>
<tr>
<td>Sat. electron drift vel. $v_a$ (10⁸ cm/s) (@ $E_c &gt; 5 \times 10^7$ V/cm)</td>
<td>1</td>
<td>0.6</td>
<td>0.75</td>
<td>2</td>
<td>2.7</td>
<td>2.7</td>
</tr>
<tr>
<td>Dielectric constant</td>
<td>11.8</td>
<td>12.8</td>
<td>12.6</td>
<td>9.7</td>
<td>9</td>
<td>5.5</td>
</tr>
<tr>
<td>Electronic $\frac{F_{RF}}{F_{dc}}$ figure-of-merit relative to Si (E_c-v_a)²</td>
<td>1</td>
<td>0.4</td>
<td>0.9</td>
<td>70</td>
<td>80</td>
<td>2000</td>
</tr>
</tbody>
</table>

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1. H. Eisele, unpublished results.
successful demonstration of power combining with D-band (110–170 GHz) Gunn devices [32], are also included in Fig. 9. RF power levels of more than 300 mW at 106 GHz, 130 mW at 136 GHz, and more than 125 mW at 152 GHz were obtained using the in-line dual-waveguide cavity configuration of Fig. 10. Oscillators with these power-combined devices maintain the same excellent phase–noise properties as those with single devices [32], and also retain some of the smooth tuning capabilities [32]. Second-harmonic power extraction from these InP Gunn devices was shown to be possible up to at least 290 GHz [10] and, as examples and preliminary results, RF power levels of more than 0.3 mW at 283 GHz [10] and 2 mW at 223 GHz were demonstrated.\(^2\)

Work has been, and is being, performed in the area of GaAs and InP IMPATT diodes, and InP is emerging as a new potential candidate for high-performance IMPATT diodes [37]. Exemplary results for simple single-drift flat-profile GaAs IMPATT diodes are RF power levels (and corresponding dc-to-RF conversion efficiencies) of 320 mW (7%) at 92 GHz and more than 10 mW (>1.0%) at 140 GHz [38], TUNNETT diodes can compete in RF output power, e.g., 100 ± 5 mW at 100–107 GHz [9], with GaAs Gunn devices around 100 GHz [29], whereas their dc-to-RF conversion efficiencies, e.g., 5.8%–6.1% at 100–107 GHz [9], are much higher than those of GaAs Gunn devices [29]. Contrary to GaAs Gunn devices [29], these GaAs TUNNETT diodes operate around 100 GHz in the fundamental mode. Since they also exhibit strongly nonlinear properties, second-harmonic power extraction can be utilized [9] and, as preliminary results, yielded RF power levels of approximately 3 mW at 217.6 GHz and more than 2 mW at 234.5 GHz, which correspond to dc-to-RF conversion efficiencies of more than 0.3% and remarkable up-conversion efficiencies of between 6% and more than 10%.\(^1\) As illustrated with the spectrum in Fig. 11 of a free-running oscillator at 217.6 GHz, the excellent phase noise of much lower than \(-87\) dBc/Hz at a frequency off the carrier of 500 kHz, correctly reflects phase noise of much lower than \(-93\) dBc/Hz, which was measured at the fundamental frequency [9].\(^1\) and similar dc bias.

Power-combining efficiencies around 80% and a combined RF power of more than 140 mW at 104 GHz [9] were achieved\(^2\), and the spectrum in Fig. 11 of a free-running oscillator at 217.6 GHz, the excellent phase noise of much lower than \(-87\) dBc/Hz at a frequency off the carrier of 500 kHz, correctly reflects phase noise of much lower than \(-93\) dBc/Hz, which was measured at the fundamental frequency [9].\(^1\) and similar dc bias.

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lowest FM noise measures (<18 dB) for any oscillator with two-terminal devices were measured with GaAs TUNNETT diodes around 100 GHz [40]. Fig. 12 also confirms the well-known low-noise properties of Gunn devices.

VIII. CONCLUSION

The power-generation capabilities of selected two-terminal devices were reviewed, and an overview of results from current research in this area was presented. It is evident from these exemplary results that significant RF power levels and low-noise performance in oscillators can be achieved with two-terminal devices operating at millimeter-wave frequencies. These devices will continue to be useful in several system applications where high power for transmitters or low noise for local oscillators is required. Improved performance such as operation at much higher millimeter-wave frequencies of TED’s or much higher RF power levels in transit-time diodes from new materials can be expected as results of future research.

REFERENCES

Heribert Eisele received the Dipl.-Ing. and Dr.-Ing. degrees from the Technical University of Munich, Munich, Germany, in 1983 and 1989, respectively, both in electrical engineering.

From 1984 to 1990, he worked as a Research Engineer and Teaching Assistant at the Lehrstuhl für Allgemeine Elektrotechnik und Angewandte Elektronik, where he was involved in IMPATT diode technology, millimeter-wave measurements, and semiconductor material characterization. In 1990, he joined the Solid-State Electronics Laboratory, University of Michigan at Ann Arbor, where he is currently involved as an Assistant Research Scientist in numerical simulations and fabrication technologies of two-terminal devices, applications of two-terminal devices as power sources at millimeter- and submillimeter-wave frequencies, and optical transmission of microwave and millimeter-wave signals. He has authored or coauthored more than 50 technical papers in journals and conference proceedings.

George I. Haddad (S’57–M’61–SM’66–F’72–LF’97) received the B.S.E., M.S.E., and Ph.D. degrees in electrical engineering from the University of Michigan at Ann Arbor, in 1956, 1958, and 1963, respectively.

In 1958, he joined the Electron Physics Laboratory, University of Michigan at Ann Arbor, where he was engaged in research on masers, parametric amplifiers, detectors, and electron-beam devices. From 1960 to 1969, he served as Instructor, Assistant Professor, Associate Professor, and Professor in the Electrical Engineering Department. He served as Director of the Electron Physics Laboratory from 1968 to 1975. From 1975 to 1986 and 1991 to 1997, he served as Chairman of the Department of Electrical Engineering and Computer Science. From 1987 to 1990, he was Director of both the Solid-State Electronics Laboratory and the Center for High-Frequency Microelectronics. He is currently the Robert J. Hiller Professor of the Electrical Engineering and Computer Science Department and Director of the Center for High Frequency Microelectronics. His current research areas are microwave and millimeter-wave solid-state devices and monolithic integrated circuits, microwave-optical interactions, and optoelectronic devices and integrated circuits.

Dr. Haddad is a member of Eta Kappa Nu, Sigma Xi, Phi Kappa Phi, Tau Beta Pi, the American Society for Engineering Education, and the American Physical Society, and the National Academy of Engineering. He received the 1970 Curtis W. McGraw Research Award of the American Society for Engineering Education for outstanding achievements by an engineering teacher, The College of Engineering Excellence in Research Award (1985), The Distinguished Faculty Achievement Award (1986) of the University of Michigan, the S. S. Attwood Award of the College of Engineering for Outstanding Contributions to Engineering Education, Research, and Administration, and the 1996 IEEE MTT-S Distinguished Educator Award.