Diode modelling From Wikipedia, the free encyclopedia

In electronics, diode modelling refers to the mathematical models used to approximate the actual behavior of real diodes to enable calculations and circuit analysis. A diode's I-V curve is nonlinear (it is well described by the Shockley diode law). This nonlinearity complicates calculations in circuits involving diodes so simpler models are often required.

This article discusses the modelling of p-n junction diodes, but the techniques may be generalized to other solid state diodes.

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Large-signal modelling

Shockley diode model

The Shockley diode equation relates the diode current I of a p-n junction diode to the diode voltage V_D . This relationship is the diode I-Vcharacteristic:

$$I = I_S \left(e^{V_D/(nV_T)} - 1 \right),$$

where I_S is the saturation current or scale current of the diode (the magnitude of the current that flows for negative V_D in excess of a few V_T , typically 10^{-12} A). The scale current is proportional to the cross-sectional area of the diode. Continuing with the symbols: V_T is the thermal voltage (kT/q), about 26 mV at normal temperatures), and n is known as the diode ideality factor (for silicon diodes n is approximately 1 to 2).

When $V_D \gg nV_T$ the formula can be simplified to:

 $I \approx I_S \cdot e^{V_D/(nV_T)}$.

This expression is, however, only an approximation of a more complex I-V characteristic. Its applicability is particularly limited in case of ultrashallow junctions, for which better analytical models exist.^[1]

Diode-resistor circuit example

To illustrate the complications in using this law, consider the problem of finding the voltage across the diode in Figure 1.

Because the current flowing through the diode is the same as the current throughout the entire circuit, we can lay down another equation. By Kirchhoff's laws, the current flowing in the circuit is

$$I = \frac{V_S - V_D}{R}.$$

These two equations determine the diode current and the diode voltage. To solve these two equations, we could substitute the current I from the second equation into the first equation, and then try to rearrange the resulting equation to get V_D in terms of V_S . A difficulty with this method is that the diode law is nonlinear. Nonetheless, a formula expressing I directly in terms of V_S without involving V_D can be obtained using the Lambert W*function*, which is the inverse function of $f(w) = we^w$, that is, w = W(f). This solution is discussed next.

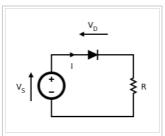


Figure 1: Diode circuit with resistive load.

Explicit solution

An explicit expression for the diode current can be obtained in terms of the Lambert W-function (also called the Omega function). A guide to these manipulations follows. A new variable w is introduced as

$$w = \frac{I_S R}{nV_T} \left(\frac{I}{I_S} + 1\right).$$

Following the substitutions $I/I_S = e^{V_D/nV_T} - 1$ and $V_D = V_S - IR$, rearrangement of the diode law in terms of w becomes

$$we^w = \frac{I_S R}{nV_T} e^{(V_s + I_s R)/(nV_T)},$$

which using the Lambert W-function becomes

$$w = W\left(\frac{I_S R}{nV_T} e^{(V_s + I_s R)/(nV_T)}\right).$$

With the approximations (valid for the most common values of the parameters) $I_s R \ll V_S$ and $I/I_S \gg 1$, this solution becomes

$$I \approx \frac{nV_T}{R} W\left(\frac{I_S R}{nV_T} e^{V_s/(nV_T)}\right).$$

Once the current is determined, the diode voltage can be found using either of the other equations.

Iterative solution

The diode voltage V_D can be found in terms of V_S for any particular set of values by an iterative method using a calculator or computer ^[2] The diode law is rearranged by dividing by I_S , and adding 1. The diode law becomes

$$\frac{I}{I_S} + 1 = e^{V_D/(nV_T)}.$$

By taking natural logarithms of both sides the exponential is removed, and the equation becomes

$$\frac{V_D}{nV_T} = \ln\left(\frac{I}{I_S} + 1\right).$$

For any I, this equation determines V_D . However, I also must satisfy the Kirchhoff's law equation, given above. This expression is substituted for I to obtain

$$\frac{V_D}{nV_T} = \ln\left(\frac{(V_S - V_D)/R}{I_S} + 1\right),$$

or

$$V_D = nV_T \ln\left(\frac{V_S - V_D}{RI_S} + 1\right).$$

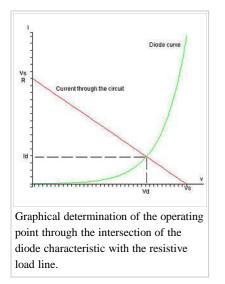
The voltage of the source V_S is a known given value, but V_D is on both sides of the equation, which forces an iterative solution: a starting value for V_D is guessed and put into the right side of the equation. Carrying out the various operations on the right side, we come up with a new value for V_D . This new value now is substituted on the right side, and so forth. If this iteration *converges* the values of V_D become closer and closer together as the process continues, and we can stop iteration when the accuracy is sufficient. Once V_D is found, I can be found from the Kirchhoff's law equation.

Sometimes an iterative procedure depends critically on the first guess. In this example, almost any first guess will do, say $V_D = 600 \text{ mV}$. Sometimes an iterative procedure does not converge at all: in this problem an iteration based on the exponential function does not converge, and that is why the equations were rearranged to use a logarithm. Finding a convergent iterative formulation is an art, and every problem is different.

Graphical solution

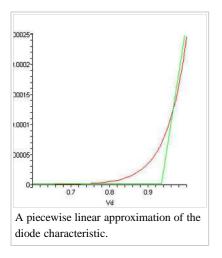
Graphical analysis is a simple way to derive a numerical solution to the transcendental equations describing the diode. As with most graphical methods, it has the advantage of easy visualization. By plotting the I-V curves, it is possible to obtain an approximate solution to any arbitrary degree of accuracy. This process is the graphical equivalent of the two previous approaches, which are more amenable to computer implementation.

This method plots the two current-voltage equations on a graph and the point of intersection of the two curves satisfies both equations, giving the value of the current flowing through the circuit and the voltage across the diode. The following figure illustrates such method.



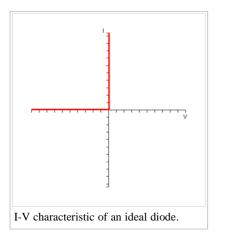
Piecewise linear model

In practice, the graphical method is complicated and impractical for complex circuits. Another method of modelling a diode is called **piecewise linear** (PWL) **modelling**. In mathematics, this means taking a function and breaking it down into several linear segments. This method is used to approximate the diode characteristic curve as a series of linear segments. The real diode is modelled as 3 components in series: an ideal diode, a voltage source and a resistor. The figure below shows a real diode I-V curve being approximated by a two-segment piecewise linear model. Typically the sloped line segment would be chosen tangent to the diode curve at the Q-point. Then the slope of this line is given by the reciprocal of the small-signal resistance of the diode at the Q-point.



Mathematically idealized diode

Firstly, let us consider a mathematically idealized diode. In such an ideal diode, if the diode is reverse biased, the current flowing through it is zero. This ideal diode starts conducting at 0 V and for any positive voltage an infinite current flows and the diode acts like a short circuit. The I-V characteristics of an ideal diode are shown below:

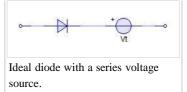


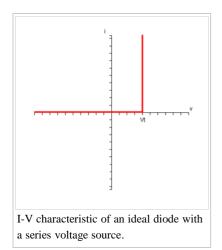
Ideal diode in series with voltage source

Now let us consider the case when we add a voltage source in series with the diode in the form shown below:

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When forward biased, the ideal diode is simply a short circuit and when reverse biased, an open circuit. If the anode of the diode is connected to 0 V, the voltage at the cathode will be at Vt and so the potential at the cathode will be greater than the potential at the anode and the diode will be reverse biased. In order to get the diode to conduct, the voltage at the anode will need to be taken to Vt. This circuit approximates the cutin voltage present in real diodes. The combined I-V characteristic of this circuit is shown below:





The Shockley diode model can be used to predict the approximate value of V_t .

$$\begin{split} I &= I_S \left(e^{V_D / (n \cdot V_T)} - 1 \right) \Leftrightarrow \\ \ln \left(1 + \frac{I}{I_S} \right) &= \frac{V_D}{n \cdot V_T} \Leftrightarrow \\ V_D &= n \cdot V_T \ln \left(1 + \frac{I}{I_S} \right) \approx n \cdot V_T \ln \left(\frac{I}{I_S} \right) \Leftrightarrow \\ V_D &\approx n \cdot V_T \cdot \ln 10 \cdot \log_{10} \left(\frac{I}{I_S} \right) \end{split}$$

Using n = 1 and T = 25C:

$$V_D \approx 0.05916 \cdot \log_{10} \left(\frac{I}{I_S} \right)$$

Typical values of the saturation current at room temperature are:

- $I_S = 10^{-12}$ for silicon diodes; $I_S = 10^{-6}$ for germanium diodes.

As the variation of V_D goes with the logarithm of the ratio $\frac{I}{I_s}$, its value varies very little for a big variation of the ratio. The use of base 10 logarithms makes it easier to think in orders of magnitude.

For a current of 1.0 mA:

- $V_D \approx 0.53V$ for silicon diodes (9 orders of magnitude); $V_D \approx 0.18V$ for germanium diodes (3 orders of magnitude).

For a current of 100 mA:

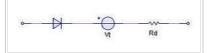
- $V_D \approx 0.65V$ for silicon diodes (11 orders of magnitude); $V_D \approx 0.30V$ for germanium diodes (5 orders of magnitude).

Values of 0.6 or 0.7 Volts are commonly used for silicon diodes ^[3]

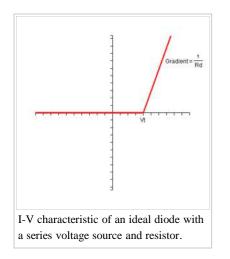
Diode with voltage source and current-limiting resistor

The last thing needed is a resistor to limit the current, as shown below:

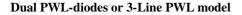
The *I*-*V* characteristic of the final circuit looks like this:

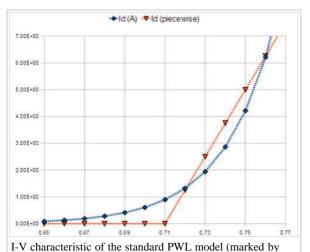


Ideal diode with a series voltage source and resistor.

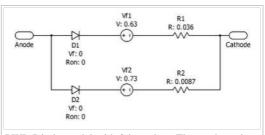


The real diode now can be replaced with the combined ideal diode, voltage source and resistor and the circuit then is modelled using just linear elements. If the sloped-line segment is tangent to the real diode curve at the Q-point, this approximate circuit has the same small-signal circuit at the Q-point as the real diode.

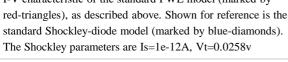


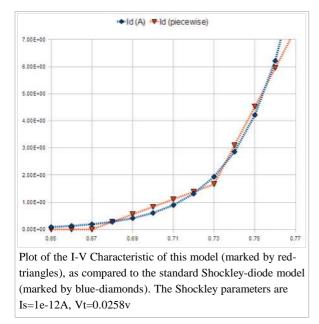


When more accuracy is desired in modelling the diode's turn-on characteristic, the model can be enhanced by doubling-up the standard PWL-model. This model uses two piecewise-linear diodes in parallel, as a way to model a single diode more accurately.



PWL Diode model with 2 branches. The top branch has a lower forward-voltage and a higher resistance. This allows the diode to switch on more gradually, and in this regard more accurately models a real diode. The bottom branch has a higher forward voltage and a lower resistance, thus allowing high current at high voltage





Small-signal modelling

Resistance

Using the Shockley equation, the small-signal diode resistance r_D of the diode can be derived about some operating point (Q-point) where the DC bias current is I_Q and the Q-point applied voltage is V_Q .^[4] To begin, the diode *small-signal conductance* g_D is found, that is, the change in current in the diode caused by a small change in voltage across the diode, divided by this voltage change, namely:

$$g_D = \frac{dI}{dV}\Big|_Q = \frac{I_0}{V_T} e^{V_Q/V_T} \approx \frac{I_Q}{V_T}.$$

The latter approximation assumes that the bias current I_Q is large enough so that the factor of 1 in the parentheses of the Shockley diode equation can be ignored. This approximation is accurate even at rather small voltages, because the thermal voltage $V_T \approx 25 \text{ mV}$ at 300K, so V_Q/V_T tends to be large, meaning that the exponential is very large.

Noting that the small-signal resistance r_D is the reciprocal of the small-signal conductance just found, the diode resistance is independent of the ac current, but depends on the dc current, and is given as

$$r_D = \frac{n * V_T}{I_Q}.$$

Capacitance

The charge in the diode carrying current I_Q is known to be

$$Q = I_Q \tau_F + Q_J,$$

where τ_F is the forward transit time of charge carriers:^[4] The first term in the charge is the charge in transit across the diode when the current I_Q flows. The second term is the charge stored in the junction itself when it is viewed as a simple capacitor; that is, as a pair of electrodes with opposite charges on them. It is the charge stored on the diode by virtue of simply having a voltage across it, regardless of any current it conducts.

In a similar fashion as before, the diode capacitance is the change in diode charge with diode voltage:

$$C_D = \frac{dQ}{dV_Q} = \frac{dI_Q}{dV_Q}\tau_F + \frac{dQ_J}{dV_Q} \approx \frac{I_Q}{V_T}\tau_F + C_J,$$

where $C_J = \frac{dQ_J}{dV_Q}$ is the junction capacitance and the first term is called the diffusion capacitance, because it is related to the current diffusing through the junction.

Variation of Forward Voltage with Temperature

The above equation has an exponential of $V_D/(kT/q)$, which would lead one to expect that the forward-voltage increases with temperature. In fact, this is generally not the case: As temperature rises, the saturation current I_S rises, and this effect dominates. So as the diode becomes *hotter*, the forward-voltage (for a given current) *decreases*.

Here is some detailed experimental experimental data (http://www.cliftonlaboratories.com/1n400x_diode_family_forward_voltage.htm#Forward_ Drop_versus_Temperature_and_Current), which shows this for a 1N4005 silicon diode. In fact, some silicon diodes are used as temperature sensors, for example the CY7 series (datasheet (http://www.omega.com/Temperature/pdf/CY7.pdf)) has a forward voltage of 1.02V in liquid nitrogen (77K), 0.54V at room temperature, and 0.29V at 100 degC.

In addition there is a small change of the material parameter bandgap with temperature. For LEDs, this bandgap change also changes their colour: they move towards the blue end of the spectrum when cooled.

Since the diode forward-voltage drops as its temperature rises, this can lead to Thermal_runaway, especially in bipolar-transistor circuits, where a change in bias leads to an increase in power-dissipation, which in turn changes the bias even further.

References

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- 4. ^ *a b* R.C. Jaeger and T.N. Blalock (2004). *Microelectronic Circuit Design* (http://books.google.com/?id=u6vH4Gsrlf0C&pg=PA883&dq=% 22Microelectronic+Circuit+Design%22+inauthor:Jaeger+small-signal+diode) (second ed.). McGraw-Hill. ISBN 0-07-232099-0.

See also

- Bipolar junction transistor
- Semiconductor device modelling

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